

**● PRINTER RUSH ●**  
(PTO ASSISTANCE)

IFW

Application : <u>09/719134</u>	Examiner : <u>Williams</u>	GAU : <u>2879</u>
From: <u>LAS</u>	Location: <u>IDC</u> FMF FDC	Date: <u>3/30/05</u>
Tracking #: <u>5988337</u>		Week Date: <u>8/2/04</u>

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input type="checkbox"/> DRW		
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input checked="" type="checkbox"/> SPEC	<u>12-7-2000</u>	

[RUSH] MESSAGE: \_\_\_\_\_  
Original page 2 is missing from the  
specification dated 12-7-2000, please provide.  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
Thank you

[XRUSH] RESPONSE: \_\_\_\_\_  
p.2 provided by attorney -  
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\_\_\_\_\_  
\_\_\_\_\_  
INITIALS: JBH

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.  
REV 10/04

WO 99/66525

PCT/JP99/03189

Address electrodes 106 and partition walls 107 are formed on a back glass substrate 105. Fluorescent substance layers 110 to 112 of respective colors (red, green, and blue) are formed in space between the partition walls 107.

5 The front glass substrate 101 is laid on the partition walls 107 on the back glass substrate 105 to form space. A discharge gas is charged into the space to form discharge spaces 109.

10 In the above PDP with such a construction, vacuum ultraviolet rays (their wavelength is mainly at 147nm) are emitted as electric discharges occur in the discharge spaces 109. The fluorescent substance layers 110 to 112 of each color are excited by the emitted vacuum ultraviolet rays, resulting in color display.

15 The above PDP is manufactured in accordance with the following procedures.

The display electrodes 102 are produced by applying silver paste to the surface of the front glass substrate 101, and baking the applied silver paste. The dielectric glass layer 20 103 is formed by applying a dielectric glass paste to the surface of the layers, and baking the applied dielectric glass paste. The protecting layer 104 is then formed on the dielectric glass layer 103.

The address electrodes 22 are produced by applying 25 silver paste to the surface of the back glass substrate 105, and

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Hiroyuki Kado

Serial No.: 09/719,134

Filed: December 7, 2000

For: PLASMA DISPLAY PANEL WITH  
SUPERIOR LIGHT-EMITTING  
CHARACTERISTICS, AND METHOD  
AND APPARATUS FOR PRODUCING  
THE PLASMA DISPLAY PANEL

Patent Examiner: Williams, Joseph L.

Group Art Unit: 2879

December 15, 2005

Costa Mesa, California 92626

**LETTER**

**VIA FACSIMILE**  
**1-703-746-4625**

ATTN: Ms. Higgins  
Publications Dept.  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Ms. Higgins:

Thank you for the phone conference on the above identified application. I reviewed the file and enclosed herewith is Page 2 which was part of the original PCT application from my file. I note that the PCT application had the specification on both sides of the page and I suspect that the US Patent Office did not scan in the rear side of Page 1.

Enclosed is my copy of Page 2. If you have any questions please do not hesitate to  
contact me.

I hereby certify that this correspondence is  
being transmitted via facsimile to 1-703-746-  
4625, Attn: Ms. Higgins of Publications,  
Commissioner for Patents, P.O. Box 1450,  
Alexandria, VA 22313-1450 on December  
15, 2005

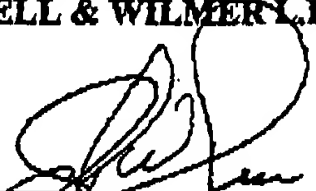
By: Sharon Farnus

  
Signature

Dated: December 15, 2005

Very truly yours,

**SNELL & WILMER L.L.P.**

  
\_\_\_\_\_  
Joseph W. Price  
Registration No. 25,124  
600 Anton Boulevard, Suite 1400  
Costa Mesa, CA 92626  
Telephone: (714) 427-7420  
Facsimile: (714) 427-7799

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